

AMENDMENTS TO THE SPECIFICATION

Please amend para [0029] as follows:

[0029] The interface circuit 54 also comprises a set of input ports 56 and a set of output ports 58. Both sets of ports, 56 and 58, are comprised of an n bit wide bus. In the preferred embodiment, the set of input ports 56 is an 88-bit wide bus that carries the data received from an incoming channel of the allocation module 34 device 18 and the set of output ports 58 is an 88-bit wide bus that carries the data to be transmitted onto the outgoing channel of the allocation module 34 device 18. Furthermore, ~~the 134-bit wide bus~~ 60 carries payload to and from the personality modules 34. The dynamic bandwidth allocation system of the present invention dynamically allocates the bits in the set of input ports 56 and the set of output ports 58 to bus 60 ~~the 134-bit wide bus~~ which is ~~are~~ hardwired to the personality modules 34 to transmit the payload. The multiplexer 48 then multiplexes the 88-bit wide format communications data from the allocation module 34 into a 32-bit wide data format to be transmitted across the single transmission channel 52 to external devices.